

## DATA ALIGNMENT COMPENSATOR

### ABSTRACT

An apparatus and method that correct skew associated with data receive from different transmission links. A known training pattern is sent through the transmission links. The training pattern is recovered and forwarded through delay registers/selecter logic to a memory buffer. A programmed controller accesses the memory and searches for the training pattern. If the training pattern is found for each transmission link, the offsets between the transmission links are determined and are used by the delay registers/selecter logic to adjust the position of the pattern so that the patterns from each link is linearly aligned within the memory buffer.